REMARKS

This application contains claims 1, 4 and 10-34. Claims 20-33 were previously withdrawn. Claim 1 is hereby amended. A new claim 34 has been added. No new matter has been introduced. Reconsideration is respectfully requested.

The disclosure was objected to due to two informalities regarding the definitions of the message stopper separator and of the "magic number" in the stopper. Applicant has amended the specification, as suggested by the Examiner, in order to overcome these objections.

Claims 1, 4 and 10-19 were rejected for non-enablement under 35 U.S.C. 112, first paragraph. Claim 1 has been amended to more clearly recite the features of the present invention.

Claim 1, as currently amended, recites the exact functionality of the write operations performed by the transmitting CPU and the receiving CPU, as described in the specification. The following table points to the appropriate text in the specification, which supports and enables each element that has been added to claim 1 in this amendment (emphasis added):

Claim 1	Specification
...wherein the receiving CPU is coupled to periodically **update the second total read register in the transmitting CPU with a content of the first total read register using one or more write operations;**	Page 14, line 27: the only exchange between the receiver 200 and the transmitter 210, … , is the **update by the receiver to the transmitter of the number of data bytes read** that is only done through the use of **write operations…** Fig. 2: Total read register 220 in receiver 200 is shown to update total read register 260 in transmitter 210.
and wherein the **transmitting CPU** is coupled to **write data** comprising **one or more message separators and one or more messages** into the circular queue, **responsively to the write head register, the second total read register, the total write register and the second queue length register.**	Page 9, line 4: **The data in memory queue 240 is written** in such a way that it contains a header-type **separator,** denoted in Fig. 2 as length and protocol ("LP"), and actual **message content,** marked as "mm"… The last data chunk is followed by a stopper-type **separator** (or stopper), which is marked as "FM,"… Page 10, line 24: the amount of memory available in memory queue 240, for

writing a message, is compared against the length of the message. This is done by subtracting the difference between the content of **total write register 270** and the content of **total read register 260** from the **queue length 285** of memory queue 240,...

Page 8, line 24: As **writes progress**, the contents of both **total write register 270** and **write head register 280** are updated.
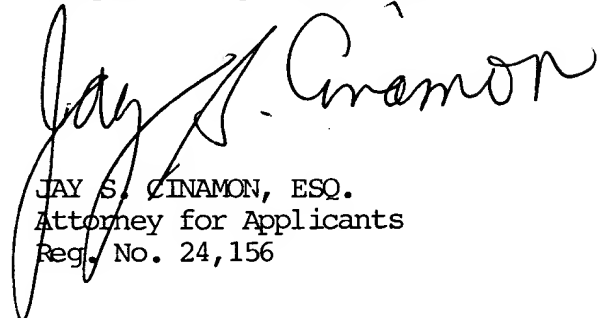
Thus, Applicant believes that claim 1 as amended meets the requirements of 35 U.S.C. 112 and is in condition for allowance. In view of the amendment to claim 1, claims 4, 10-19 and 34, which depend from claim 1, are believed to be allowable, as well.

Applicant has added new dependent claim 34 to more completely recite the features of the present invention. This claim provides details of the method used by the transmitting CPU in determining whether sufficient memory space is available in the circular queue of the receiving CPU for writing the data. The claim finds literal support in the specification on page 10, line 24: "...the amount of memory available in memory queue 240, for writing a message, is compared against the length of the message. This is done by subtracting the difference

between the content of total write register 270 and the content of total read register 260 from the queue length 285 of memory queue 240, and in step 320 comparing the result with the length of the message." See also step 320 in Fig. 3.

Applicant believes the amendments and remarks presented hereinabove to be fully responsive to all of the objections and grounds of rejection raised by the Examiner. In view of these amendments and remarks, Applicant respectfully submits that all of the claims remaining in the present application are in order for allowance. Notice to this effect is hereby requested.

Respectfully submitted,

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